

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	22952748	@ad<"20020802"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 13:39
L2	2	1 and SIMM same DIMM same (socket terminal) same (line connect\$3 bus\$3) same (length long\$2 short\$2) same (delay\$3 shift\$3 tim\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 18:28
L3	12	1 and SIMM same DIMM same (line connect\$3 bus\$3) same (length long\$2 short\$2) same (delay\$3 shift\$3 tim\$3 compensat\$3 balanc\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 16:05
L4	23	1 and SIMM with DIMM same (length long\$2 short\$2) same (delay\$3 shift\$3 tim\$3 compensat\$3 balanc\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:13
L5	3	1 and SIMM with DIMM same (length long\$2 short\$2) same (delay\$3 shift\$3 tim\$3 compensat\$3 balanc\$3) same (load\$3 resist\$4 imped\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:15
L6	3	1 and SIMM same DIMM same (length long\$2 short\$2) same (delay\$3 shift\$3 tim\$3 compensat\$3 balanc\$3) same (load\$3 resist\$4 imped\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:19
L7	3	1 and SIMM with DIMM same (differ\$3 larger greater smaller lesser) near4 (load\$3 resist\$4 imped\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 18:29

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L8	3	1 and SIMM same DIMM same (differ\$3 larger greater smaller lesser) with (load\$3 resist\$4 imped\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:26
L9	813	711/115.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:26
L10	271	711/101.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:27
L11	1561	711/100.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:32
L12	2	"20040024966".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:43
L13	28793	"710".clas.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:44
L15	390	710/6.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:45

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L16	1210	710/5.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:50
L17	457	710/58.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:58
L18	150	439/631.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:58
L19	1996	710/305.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 17:59
L20	48230	(lee.in. so.in. park.in.)	US-PGPUB	OR	ON	2007/03/22 18:00
L21	0	1 and 20 and "SIMM".clm. and "DIMM".clm.	US-PGPUB	OR	ON	2007/03/22 18:01
L22	0	1 and 20 and "SIMM".clm.	US-PGPUB	OR	ON	2007/03/22 18:02
L23	280	1 and 20 and length.clm. and line.clm.	US-PGPUB	OR	ON	2007/03/22 18:03
L24	0	1 and 20 and length.clm. and line.clm. and simm.clm. and dimm.clm.	US-PGPUB	OR	ON	2007/03/22 18:04
L25	0	1 and 20 and length.clm. and line.clm. and simm.clm.	US-PGPUB	OR	ON	2007/03/22 18:05
L26	2	(9 10 11 13 15 16 17 18 19) and (2 3 4 5 6 7 8)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 18:38

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L27	1216	1 and (differ\$3 larger greater smaller lesser) near2 (load\$3 resist\$4 imped\$4) same (module device) same memor\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 18:31
L28	2	1 and (differ\$3 larger greater smaller lesser) near2 (load\$3 resist\$4 imped\$4) same (module device) same memor\$3 same controller same (length long\$2 short\$2) same (line connect\$3 bus\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 18:36
L29	88	1 and (differ\$3 larger greater smaller lesser) near2 (load\$3 resist\$4 imped\$4) same (module device) same memor\$3 same (length long\$2 short\$2) same (line connect\$3 bus\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 18:36
L30	51	(9 10 11 13 15 16 17 18 19) and (2 3 4 5 6 7 8 27 28 29)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 18:39
L31	6	(9 10 11 13 15 16 17 18 19) and (2 3 4 5 6 7 8 28 29)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 18:39


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<u>#3</u> ((simm dimm<in>metadata) <and> (longer length<in>metadata))<and> (delay<in>metadata)
<u>#4</u> ((delay<in>metadata) <and> (loads<in>metadata))<and> (timing<in>metadata)
<u>#5</u> ((socket<in>metadata) <and> (loads<in>metadata))<and> (delay<in>metadata)
<u>#6</u> ((different loads<in>metadata) <and> (length<in>metadata)) <and> (time<in>metadata)
<u>#7</u> ((simm<in>metadata) <and> (dimm<in>metadata))
<u>#8</u> ((simm<in>metadata) <and> (dimm<in>metadata))
<u>#9</u> ((simm<in>metadata) <and> (dimm<in>metadata))<and> (delay<in>metadata)
<u>#10</u> ((loads<in>metadata) <and> (lengths<in>metadata))<and> (time<in>metadata)
<u>#11</u> ((different loads<in>metadata) <and> (different lengths<in>metadata))<and> (time<in>metadata)

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Terms used

SIMM DIMM length time delay line terminal socket load impedance resistance

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 Michael Doggett, Michael Meißner, Urs Kanus

July 1999 **Proceedings of the ACM SIGGRAPH/EUROGRAPHICS workshop on Graphics hardware HWWS '99**

Publisher: ACM Press

Full text available:  [pdf\(1.12 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: graphics hardware, memory architecture, raycasting, volume rendering accelerator, volume visualization

2 Linux Clusters at NIST 

Wayne J. Salamon, Alan Mink

June 1999 **Linux Journal**

Publisher: Specialized Systems Consultants, Inc.

Full text available:  [html\(19.49 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

NIST is using Linux clusters for research, benchmarking them against super computers

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